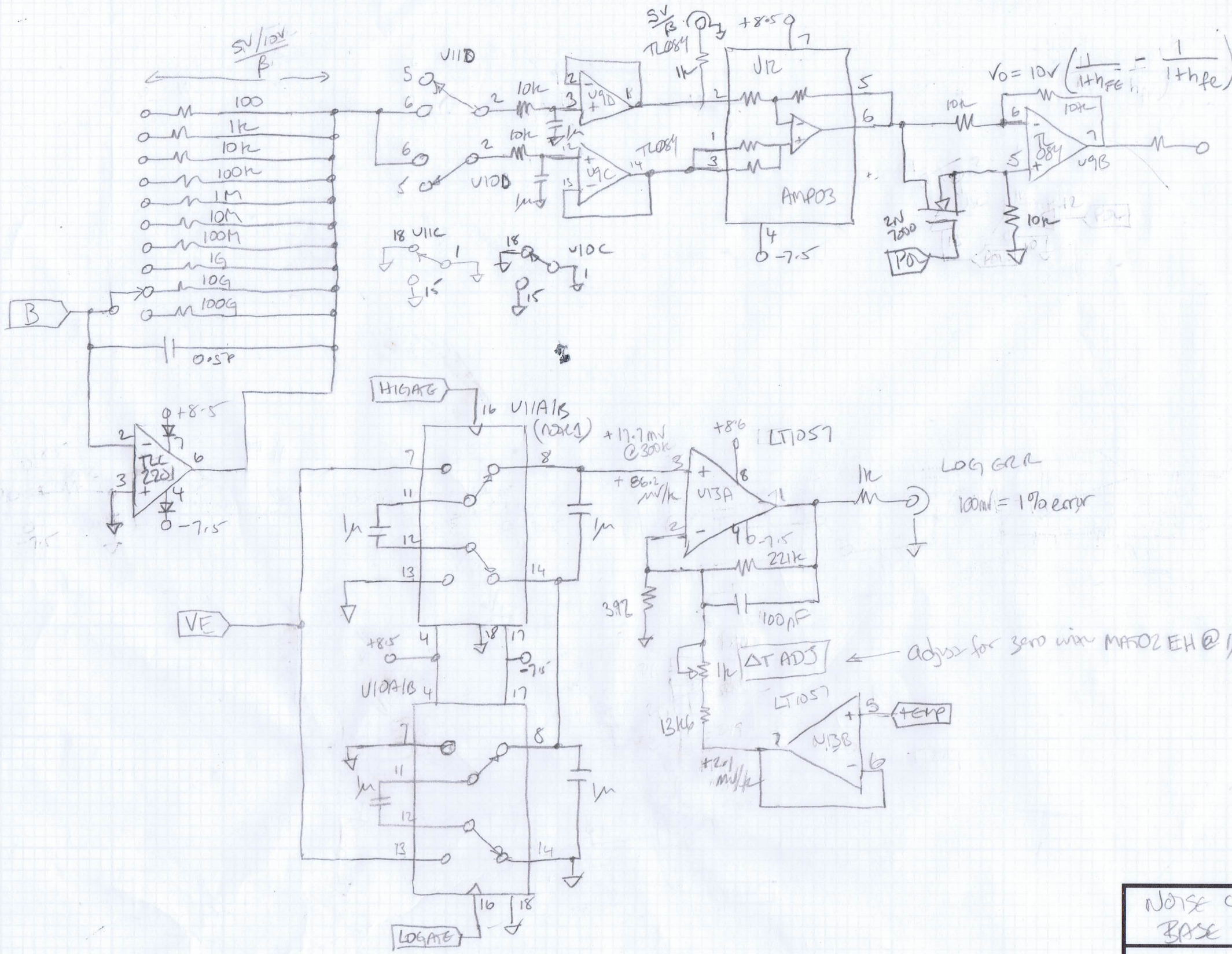
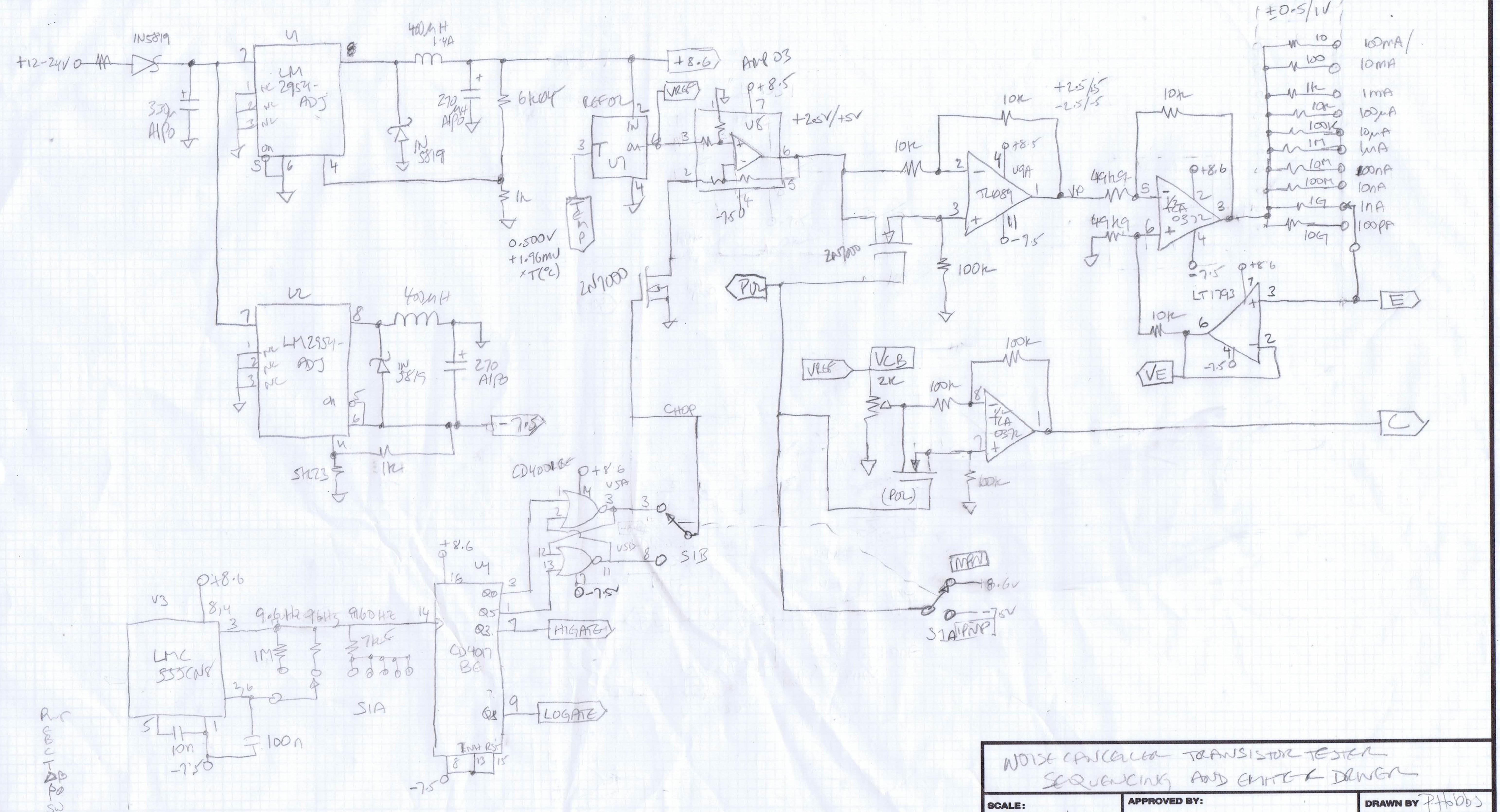


NOTES

1: Polarities are shown for clock pin 'LOW'.

NOISE CANCELLER TRANSISTOR TESTER
BASE TIA & LOG CONFORMANCE SECTIONS

SCALE:	APPROVED BY:	DRAWN BY T Hobbs
DATE: 7/6/13		REVISED
		DRAWING NUMBER



NOISE CANCELLER TRANSISTOR TESTER
SEQUENCING AND EMITTER DRIVER

SCALE:	APPROVED BY:	DRAWN BY P. Hobbs
DATE: 7/6/13		REVISED
		DRAWING NUMBER